

WEST**Freeform Search****Database:**

US Patents Full-Text Database
 US Pre-Grant Publication Full-Text Database
 JPO Abstracts Database
 EPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Term:

L12 and peugh and ((top or bottom) same memory)

Display:

10

Documents in Display Format:

TI

Starting with Number

1

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Search History**DATE:** Tuesday, February 11, 2003 [Printable Copy](#) [Create Case](#)**Set Name Query**

side by side

Hit Count Set Nam

result set

DB=USPT; PLUR=YES; OP=OR

<u>L13</u>	L12 and peugh and ((top or bottom) same memory)	13	<u>L13</u>
<u>L12</u>	((711/\$)!.CCLS.)	13901	<u>L12</u>
<u>L11</u>	L10 and ((top or bottom) same memory)	0	<u>L11</u>
<u>L10</u>	6480948.pn.	1	<u>L10</u>
<u>L9</u>	L6 and (internal\$2 same memor\$3)	1	<u>L9</u>
<u>L8</u>	L6 and (internal\$2)	1	<u>L8</u>
<u>L7</u>	L6 and (internal\$2 adj3 memor\$3)	0	<u>L7</u>
<u>L6</u>	6262594.pn.	1	<u>L6</u>
<u>L5</u>	(system-on-chip or system-on-a-chip) and (internal\$2 with rom) and (external\$3 with (flash or nonvolatile or non-volatile))	4	<u>L5</u>
<u>L4</u>	(system-on-chip or system-on-a-chip) and rom and (flash or nonvolatile or non-volatile)	37	<u>L4</u>
<u>L3</u>	L2 and alias\$3	1	<u>L3</u>
<u>L2</u>	L1 and (system-on-chip or system-on-a-chip)	18	<u>L2</u>
<u>L1</u>	((711/\$)!.CCLS.)	13901	<u>L1</u>

END OF SEARCH HISTORY